POINTS OF VIEW ON ASYNCHRONOUS COMPUTABILITY

SAMUEL MIMRAM

École Polytechnique / PPS (with É. Goubault and C. Tasson)



WIP 2016

April 27, 2016

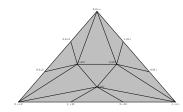
Asynchronous computability

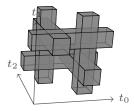
In the 90s, Herlihy et al. have obtained major results on asynchronous computability.

- What can a bunch of processes computing in parallel can compute in the presence of failures?
- ► For instance, they show that the consensus cannot be solved.
- Their proofs uses geometric arguments, they construct a geometric object corresponding to the possible states and
 - characterize those which can occur and their properties
 - obtain impossibility results from the fact that some maps should preserve (n-)connectivity
- ► The devil lies in the details.

Unifying points of view

Here, we unify different points of view on executions:





protocol complex [Herlihy, ...] geometric semantics [Goubault, ...]

$$\langle u_i, s_i \mid u_i u_j = u_j u_i, s_i s_j = s_j s_i \rangle$$

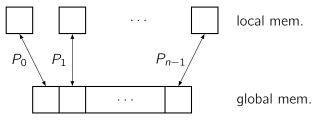
partially commutative traces



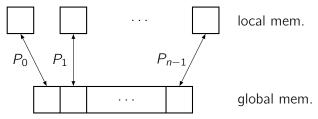
interval orders

ASYNCHRONOUS PROTOCOLS AND TASKS

- each process has a local memory cell
- ▶ there is a global memory with *n* cells

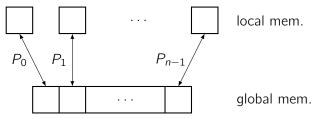


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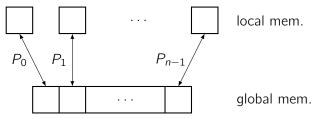
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 - update: write in its global memory cell
 - scan: read the whole global memory and update its local cell (*immediate snapshot*)

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- each process alternatively does "rounds" made of
 - update: write in its global memory cell
 - scan: read the whole global memory and update its local cell (*immediate snapshot*)
- ► at any instant a process might die
- and the question is: what we can compute in such a model? (for this question we are only interested in local memories)

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- we are mostly interested in local memory: it contains the input and output values
- \blacktriangleright the initial value for global memory is \bot in every cell

Coherence between views

The main idea here is to introduce a semantics based on the same principles as in (hyper)coherence spaces.

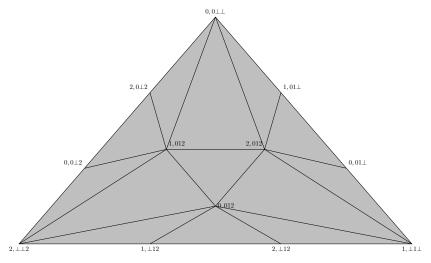
A set $X \subseteq \{(i, x) \mid i \in \mathbb{N}, x \in \mathcal{V}\}$ of local memories (= views) $(i, x) \in \mathbb{N} \times \mathcal{V}$ is **coherent** when

$$X = \{(i, l_i)\}$$

such that there is an execution leading to a local memory *I*.

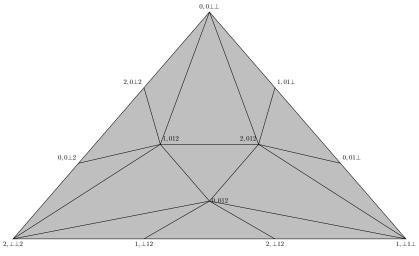
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Notice that it is simply connected.

States

Formally, we suppose fixed a number $n \in \mathbb{N}$ of processes and a set \mathcal{V} of **values** with

- $\mathcal{I} \subseteq \mathcal{V}$: input values
- $\mathcal{O} \subseteq \mathcal{V}$: output values
- $\perp \in \mathcal{I} \cap \mathcal{O}$: the undefined value / a non-participating process

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- $I \in \mathcal{V}^n$: the local memories
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The *standard* initial state has $l_i = i$ and $m_i = \bot$.

Protocols

A **protocol** π consists of, for $0 \le i < n$,

•
$$\pi_{u_i}: \mathcal{V} \to \mathcal{V}$$

the values it will write in its global memory cell depending on its local memory

$$\blacktriangleright \ \pi_{s_i}: \mathcal{V} \times \mathcal{V}^n \to \mathcal{V}$$

the values it will write in its local memory depending on the values of its local memory and all the global memory cells

such that

•
$$\pi_{s_i}(x, m) = x$$
 for $x \in \mathcal{O}$

once we decide an output we don't change our mind

The set of possible actions is

$$\mathcal{A} = \{u_i, s_i, d_i \mid 0 \leq i < n\}$$

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 $\operatorname{proj}_i(T) \in (u_i s_i)^*(\varepsilon + u_i d_i)$

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Given a protocol π , its **semantics**

$$\llbracket T \rrbracket_{\pi} : \mathcal{V}^n \times \mathcal{V}^n \to \mathcal{V}^n \times \mathcal{V}^n$$

is defined on a trace $\mathcal{T}\in\mathcal{A}^*$ by

•
$$[[u_i]]_{\pi}(l, m) = (l, m[i \leftarrow \pi_{u_i}(l_i)])$$

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• $[[T \cdot T']]_{\pi} = [[T']]_{\pi} \circ [[T]]_{\pi}$

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$$\llbracket \varepsilon \rrbracket_{\pi} = \mathsf{id}$$

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With two processes executing one round each there are "essentially" three traces:

- $\blacktriangleright u_0 s_0 u_1 s_1$:
 - P_0 does not see what P_1 has written
 - P_1 sees what P_0 has written

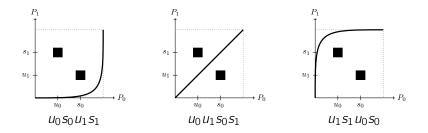
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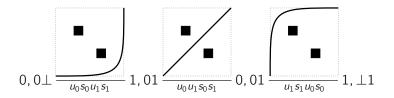
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These execution traces can be represented geometrically by



We'll get back to this representation later on.

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Tasks

A **task** θ is a relation $\theta \subseteq \mathcal{I}^n \times \mathcal{O}^n$ such that for every *I*, $I' \in \Theta$

- $I_i = \bot$ if and only if $I'_i = \bot$,
- ► there exists $I'' \in \mathcal{O}^n$ such that $(I, I'') \in \Theta$ and $(I[i \leftarrow \bot], I''[i \leftarrow \bot]) \in \Theta$.

We write dom Θ for the possible input values and codom Θ for the possible output values.

The binary consensus

In the binary consensus problem each process

- ▶ starts with a value in {0, 1}
- end with the same value, among the initial values of the alive processes.

For instance, with n = 2, we have

$$\Theta = \{(b \bot, b \bot), (\bot b, \bot b), (bb', bb), (b'b, bb) \mid b, b' \in \{0, 1\}\}$$

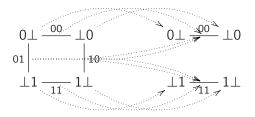
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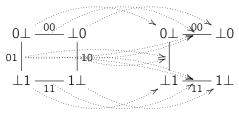
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The binary quasi-consensus

In the case n = 2, we can also consider the **binary quasi-consensus**, which is similar but restricts the output so that it cannot happen that P_1 decides 0 and P_0 decide 1 at the same time:

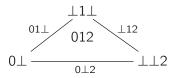


The way we draw tasks

Note that

- if $l \in \operatorname{dom} \Theta$ (the possible input values) then
 - $I[i \leftarrow \bot]$ also belongs to dom Θ

dom Θ can thus be pictured as a *simplicial complex* called the **input complex**:



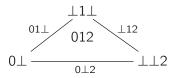
i.e. roughly a space made of triangles, tetrahedra, etc. (and similarly codom Θ gives rise to the **output complex**)

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Note also that the vertices are **colored** by $0 \le i < n$: the only active process

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1. $I_i = \bot$ if and only if $I'_i = \bot$,

2. there exists $I'' \in \mathcal{O}^n$ such that $(I, I'') \in \Theta$ and

 $(I[i \leftarrow \bot], I''[i \leftarrow \bot]) \in \Theta.$

which means

- 1. *n*-simplices are in relation with *n*-simplices
- 2. the relation is compatible with faces

Solving tasks

A protocol π **solves** a task Θ when

- ▶ for every initial local memory $I \in \operatorname{dom} \Theta$
- \blacktriangleright for every long enough and fair execution trace T

we have $l' \in \operatorname{codom} \Theta$, where

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For instance,

- the consensus cannot be solved
- the quasi-consensus can be solved

Let's understand why.

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For simplicity, we will suppose that $l_i = i$ initially (standard state) and thus write $[T]_{\pi}$ instead of $[T]_{\pi}(01...(n-1), \bot \bot ... \bot)$.

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Let's understand why.

A more manageable setting

In order to study tasks which can be solved by protocols we should simplify as much as possible what we consider as

- protocols
- execution traces

Restricting executions

It can be shown that we can, without loss of generality, restrict to traces which are

► well-bracketed:

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In particular, we have a notion of *round*.

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immediate snapshot:

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Full-information protocols

A protocol is **full-information** when

 $\pi_{u_i} = \operatorname{id}_{\mathcal{V}}$

We can restrict to those without loss of generality (and we will).

A category of protocols

A morphism $\phi:\pi
ightarrow \pi'$ between protocols consists of functions

• $\phi_i: \mathcal{V} \to \mathcal{V}$ translating memory

such that

and

$$\begin{array}{c} \mathcal{V} \times \mathcal{V}^{n} \xrightarrow{\pi_{s_{i}}} \mathcal{V} \\ \phi_{i} \times \prod_{i} \phi_{i} \middle| & & \downarrow \phi_{i} \\ \mathcal{V} \times \mathcal{V}^{n} \xrightarrow{\pi_{s_{i}}} \mathcal{V} \end{array}$$

We say that π' simulates π .

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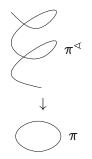
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Actually, we only require ϕ_i and ϕ'_i to be defined on *reachable* values for a given task.

Theorem (GMT)

The category of protocols admits an initial object π^{\triangleleft} .

Morally, the space of executions of π^{\triangleleft} is the "universal cover" of the space of executions of any process π : every execution of π corresponds to a unique execution of π^{\triangleleft} .



We suppose that \mathcal{V} is countable so that we have an encoding $\langle x, y \rangle$ of pairs (and uples).

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The initial object π^{\triangleleft} is called the $\mathbf{view} \ \mathbf{protocol}$ and is defined by

•
$$\pi_{u_i}^{\triangleleft}(x) = x$$
 for $x \in \mathcal{V}$ (full-information),

•
$$\pi_{s_i}^{\triangleleft}(x, m) = \langle x, \langle m \rangle \rangle$$
 for $(x, m) \in \mathcal{V} \times \mathcal{V}^n$.

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Given a trace T, the local memory of *i*-th process after executing the trace T is called its **view**.

Theorem (GMT)

The category of protocols admits an initial object π^{\triangleleft} with $\pi_{s_i}^{\triangleleft}(x, m) = \langle x, \langle m \rangle \rangle$.

Proof.

Suppose given a reachable memory

$$x = I_i$$
 with $(I, m) = \llbracket T \rrbracket_{\pi^{\triangleleft}}$

Because of the definition of morphisms, we are forced to define

$$\phi_i(x) = l'_i$$
 with $(l', m') = \llbracket T \rrbracket_{\pi}$

It only remains to check that this definition is well-defined, i.e. it does not depend on the chosen trace T...

THE PROTOCOL COMPLEX

Given a number r of rounds for each process, the **protocol complex** $\chi^r(\Theta)$ is the abstract simplicial complex whose

- vertices are x ∈ V such that x is the view (= local memory) of *i*-th process after executing a trace with π[⊲]
- simplices are sets of vertices occurring together after a same execution.

Suppose that we have 2 processes and the input is the standard one:



The protocol complex $\chi^1(\Theta)$ for 1 round is as follows:

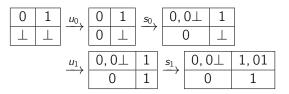
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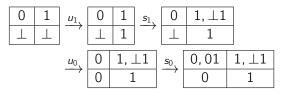


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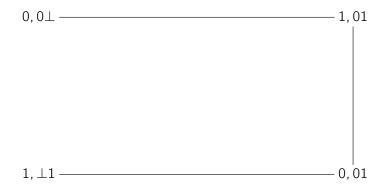
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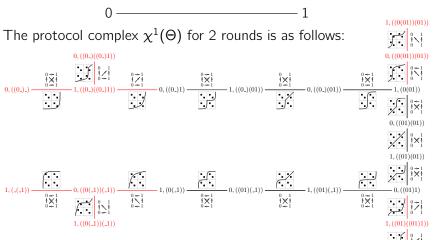


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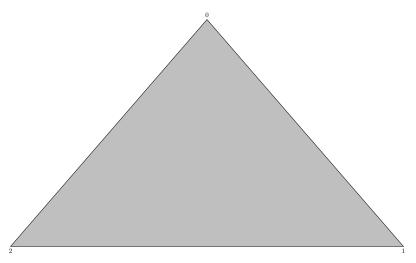
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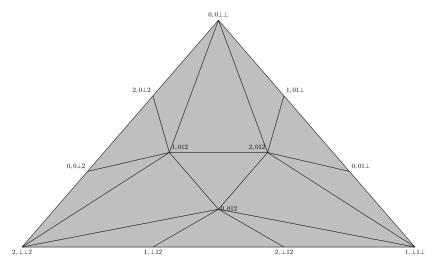


0, ((01)((01)1))

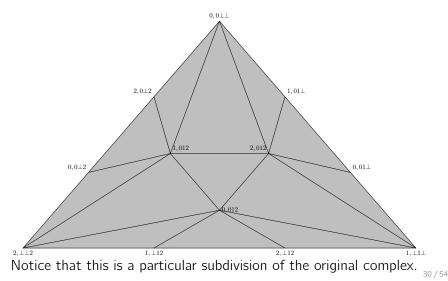
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The chromatic subdivision

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- starting from the input complex
- performing a chromatic subdivision of it r times

and this subdivision can be defined and studied independently.

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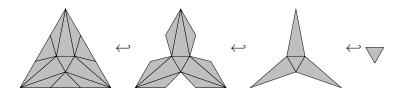
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Theorem (Herliy-Shavit, GMT, Koszlov)

If the input complex is contractible then the protocol complex is (in fact, collapsible).



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▶ it can be solved in *r* rounds

Suppose that a task Θ can be solved by a protocol π :

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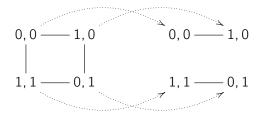
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NB: simplicial maps preserve contractibility!

The binary consensus

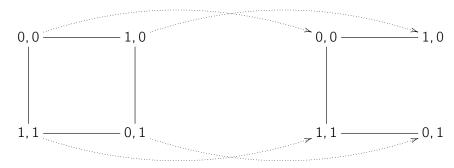
Consider again the **binary consensus** task:



There can be no protocol solving it (even after some rounds).

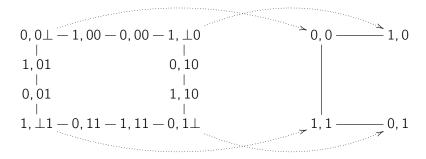
The binary quasi-consensus

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EQUIVALENCE BETWEEN TRACES

The (well-bracketed) execution traces in $\{u_i, s_i\}^*$ are semantically invariant under the congruence \approx generated by

$$u_j u_i \approx u_i u_j$$
 $s_j s_i \approx s_i s_j$

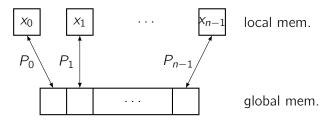
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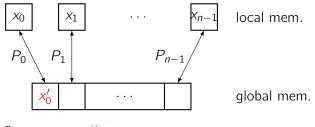
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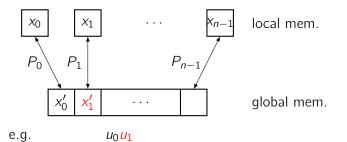


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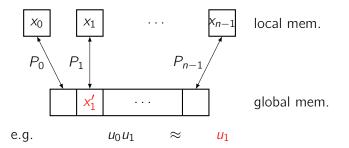
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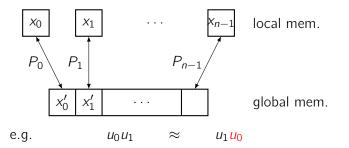
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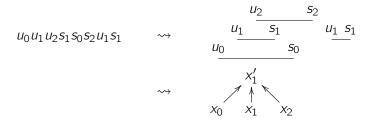
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In a well-bracketed trace, the u_i and s_i form intervals:

$$u_0 u_1 u_2 s_1 s_0 s_2 u_1 s_1 \qquad \rightsquigarrow \qquad \qquad \begin{array}{c} u_1 & \underbrace{u_2 & s_2} \\ u_1 & \underbrace{s_1} & u_1 & \underbrace{s_1} \\ u_0 & \underbrace{s_0} & \underbrace{u_1 & s_1} \end{array}$$

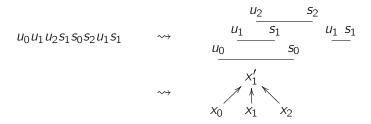
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An **interval order** (X, \preceq) is a poset such that there exists a function $I: X \to \wp(\mathbb{R})$ associating an interval I_x to each x in such a way that

$$x \prec y$$
 if and only if $\forall s \in I_x, \forall t \in I_y, s < t$

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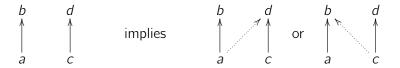
An **interval order** (X, \leq) is a poset such that there exists a function $I : X \to \wp(\mathbb{R})$ associating an interval I_x to each x in such a way that

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There is a *colored variant* with $\ell : X \to \mathbb{N}$ such that $\ell(x) = \ell(y)$ implies that x and y are comparable.

Remark (Fishburn)

A poset is an interval order if it is "(2+2)-free":



Theorem

Well-bracketed traces up to equivalence are in bijection with colored interval orders.

 $U_0 U_1 U_2 S_1 S_0 S_2 U_1 S_1 \qquad \rightsquigarrow$

 X_0 Xэ

Suppose given two elements x_i and x_j of an interval order. We have the following possible situations:

which correspond to the following traces:

$$U_i S_i U_j S_j$$
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In the two first cases, s_i sees u_i .

This suggests defining the *i*-view of a colored interval order (X, \preceq) by

- 1. restricting to elements which are below or independent from the maximum element x_i^k labeled by *i*
- 2. remove dependencies from x_i^k

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Theorem

- ▶ an interval order can be reconstructed from all the i-views
- ► the execution of the i-th process in the view protocol π[⊲] is uniquely determined by the i-view

For instance, with two processes, consider $u_0 u_1 s_1 u_1 s_0 s_1 u_0 s_0$:

▶ it corresponds to the colored interval order

$$\begin{array}{c} x_0^1 \leftarrow x_1^1 \\ \uparrow & \searrow \uparrow \\ x_0^0 & x_1^0 \end{array}$$

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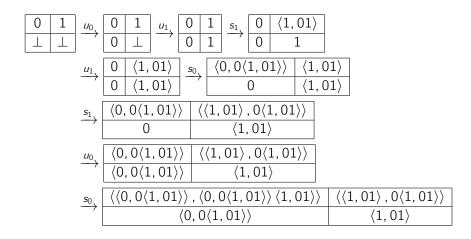
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 $\left<\left<0,0\left<1,01\right>\right>$, $\left<0,0\left<1,01\right>\right>$ $\left<1,01\right>$

 $\langle\langle 1,01
angle$, 0 $\langle 1,01
angle\rangle$

Completeness results

From this we deduce:

Theorem The equivalence is complete: given two traces t and t'

 $t \approx t'$ iff $\llbracket t
rbracket_{\pi^{\triangleleft}} = \llbracket t'
rbracket_{\pi^{\triangleleft}}$

Theorem

 π^{\triangleleft} is actually initial in the category of protocols.

The interval order complex

Definition

The interval order complex is the simplicial complex whose

- vertices are (i, V_i) where V_i is an *i*-view
- maximal simplices are {(0, V₀),..., (n, V_n)} such that there is an interval order (X, ≺) (with given number of rounds) whose *i*-view is V_i.

Theorem

The interval order complex is isomorphic to the protocol complex.

DIRECTED GEOMETRIC SEMANTICS

Directed geometric semantics

The idea of geometric semantics is to formalize the dictionary:

program	\Leftrightarrow	topological space
state	\Leftrightarrow	point of the space
execution trace	\Leftrightarrow	path
equivalent traces	\Leftrightarrow	homotopic paths

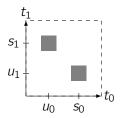
so that we can import tools from (algebraic) topology in order to study concurrent programs.

We actually need to use spaces equipped with a notion of direction in order to take in account irreversible time.

Consider two processes executing one round of update/scan, i.e.

 $u_0.s_0 \parallel u_1.s_1$

The geometric semantics of this program will be

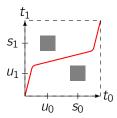


i.e. a square $[0, 1] \times [0, 1]$ minus two holes, which is directed componentwise.

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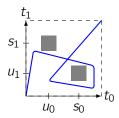
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directed path : $u_1 u_0 s_0 s_1$

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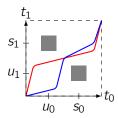
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non directed path : ???

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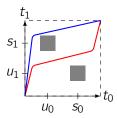
i.e. a square $[0, 1] \times [0, 1]$ minus two holes, which is directed componentwise.

homotopy between paths : $u_1 u_0 s_0 s_1 \approx u_0 u_1 s_0 s_1$

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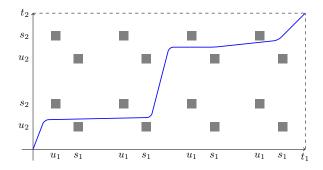
some paths are not homotopic

More examples

This generalizes to *more rounds*: consider two processes executing 2 and 4 rounds of update/scan,

```
u_0.s_0.u_0.s_0 \parallel u_1.s_1.u_1.s_1.u_1.s_1
```

The geometric semantics of this program will be



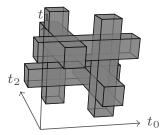
More examples

This generalizes to *more processes*:

consider three processes executing one round of update/scan,

$$u_0.s_0 \parallel u_1.s_1 \parallel u_2.s_2$$

The geometric semantics of this program will be



NB: we will illustrate in dimension 2, where things are simpler

Directed spaces

Formally,

Definition

A **pospace** (X, \leq) consists of a topological space X equipped with a partial order $\leq \subseteq X \times X$, which is closed.

A **dipath** p is a continuous non-decreasing map $p : [0, 1] \rightarrow X$.

A **dihomotopy** *H* from a path *p* to a path *q* is a continuous map $H : [0, 1] \times [0, 1] \rightarrow X$ such that

- H(0, t) = p(t) for every t
- H(1, t) = q(t) for every t
- $t \mapsto H(s, t)$ is a dipath for every s
- $s \mapsto H(s, 0)$ and $s \mapsto H(s, 1)$ are constant



Directed paths vs traces

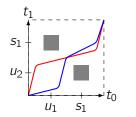
Theorem

Fixing a number of rounds for each process, there is a bijection between

(i) directed paths up to directed homotopy in the geometric semantics

 \Leftrightarrow

(iii) execution traces up to \approx



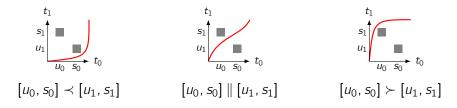
 $u_1 u_0 s_0 s_1 \approx u_0 u_1 s_0 s_1$

Directed paths vs traces

Theorem

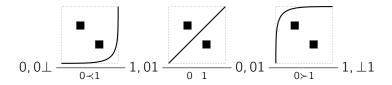
Fixing a number of rounds for each process, there is a bijection between

- (i) directed paths up to directed homotopy in the geometric semantics
- (ii) colored interval orders
- (iii) execution traces up to \approx



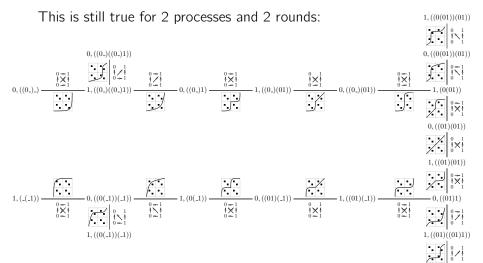
From geometry to the complex

One can notice in the last example that edges are in bijection with directed paths up to homotopy (and with interval orders):



(more generally maximal simplices are in bijection with maximal directed paths up to homotopy).

From geometry to the complex



0, ((01)((01)1))

CONCLUSION

Perspectives

Links with game stuff?

async. comp.	game semantics
update	question
scan	answer
view	view :)
interval order	event structure
trace up to equivalence	asynchronous transition system

Links with quantum stuff?

async. comp.	quantum

