Trace Spaces: an Efficient New Technique for State-Space Reduction

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Goal

When verifying a concurrent program, there is a priori a large number of possible interleavings to check (exponential in the number of processes)

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Many executions are equivalent:
we want here to provide a *minimal number of execution traces*which describe all the possible cases
by adopting a **geometric** point of view

Programs generate trace spaces

Consider the program

$$x:=1;y:=2 | y:=3$$

It can be scheduled in three different ways:

$$y:=3;x:=1;y:=2$$

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Giving rise to the following graph of traces:

$$y:=3 \xrightarrow{\begin{array}{c} x:=1 \\ y:=3 \end{array}} \xrightarrow{y:=2} \\ y:=3 \\ \hline x:=1 \\ y:=2 \end{array}$$

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Giving rise to the following graph of traces:

homotopy: commutation / filled square

- P_a : lock the mutex a
- V_a: unlock the mutex a

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$$P_b; x:=1; V_b; P_a; y:=2; V_a \mid P_a; y:=3; V_a$$

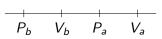
- P_a : lock the mutex a
- V_a : unlock the mutex a

$$P_b.V_b.P_a.V_a \mid P_a.V_a$$

Let's adopt a geometric point of view!

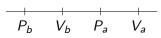
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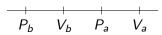


 \bullet $P_a.V_a$



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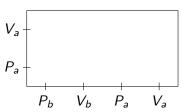
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• $P_a.V_a$

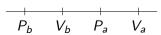


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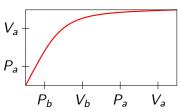
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• $P_a.V_a$

$$P_a$$
 V_a

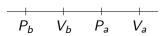
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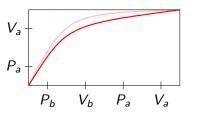


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 V_a

• $P_b.V_b.P_a.V_a \mid P_a.V_a$

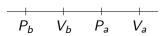
Homotopy



$$P_a.P_b.V_a.V_b.P_a.V_a$$

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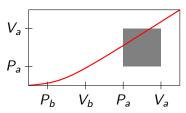
• $P_b.V_b.P_a.V_a$



• $P_a.V_a$



• $P_b.V_b.P_a.V_a \mid P_a.V_a$

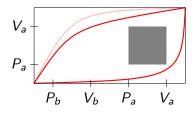


 $P_b.V_b.P_a.P_a.V_a.V_a$

Forbidden region

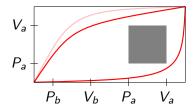
Trace

A trace is the homotopy class of a path.



Trace

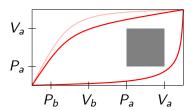
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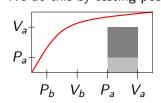
Trace

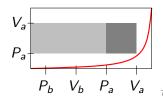
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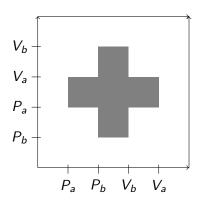
We want to compute a path in every trace

We do this by testing possible ways to go around forbidden regions:



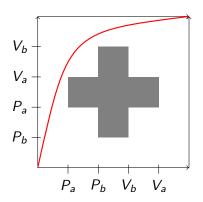


$$P_a.P_b.V_b.V_a \mid P_b.P_a.V_a.V_b$$



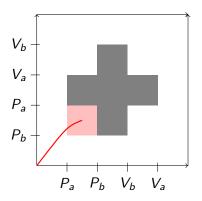
A forbidden region

$$P_a.P_b.V_b.V_a \mid P_b.P_a.V_a.V_b$$



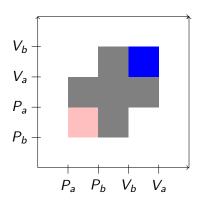
A path: $P_b.P_a.V_a.P_a.V_b.P_b.V_b.V_a$

$$P_a.P_b.V_b.V_a \mid P_b.P_a.V_a.V_b$$



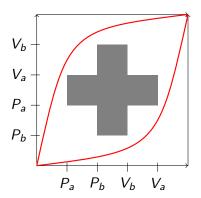
A deadlock: P_b.P_a

$$P_a.P_b.V_b.V_a \mid P_b.P_a.V_a.V_b$$



An unreachable region

$$P_a.P_b.V_b.V_a \mid P_b.P_a.V_a.V_b$$



Here we are interested in maximal paths modulo homotopy

Plan

- 1 Trace semantics of programs
- ② Geometric semantics of programs
- **3** Computation of the trace space

Programs

We consider programs of the form:

$$p$$
 ::= $\mathbf{1}$ | P_a | V_a | $p.p$ | $p|p$ | $p+p$ | p^*

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We omit non-deterministic choice, loops and thread creation:

The trace semantics of a program will be an **asynchronous graph**:

- a graph G = (V, E) labeled by actions
- with an independence relation I



relating paths of length 2

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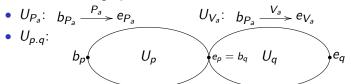
relating paths of length 2

together with a beginning and an end vertex

Homotopy is the smallest congruence on paths containing 1.

To every program p we associate (U_p, b_p, e_p) defined by:

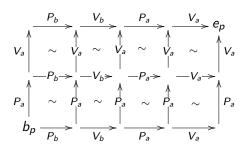
• U1: terminal graph



• $U_{p|q}$ is the "cartesian product" of U_p and U_q :

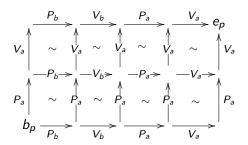
Example:

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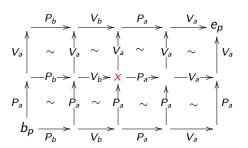


The **resource function** r_a associates to every vertex x:

number of releases of a - number locks of a

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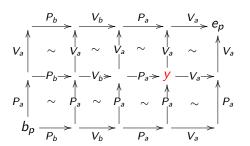
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Ex:
$$r_a(x) = -1$$
, $r_b(x) = 0$

Trace semantics

Example:

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Ex:
$$r_a(y) = -2$$
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Trace semantics

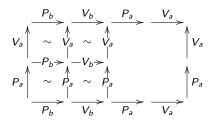
Trace semantics T_p :

 U_p where we remove vertices x which do not satisfy

$$-1 \leqslant r_a(x) \leqslant 0$$

Example:

$$P_b.V_b.P_a.V_a \mid P_a.V_a$$



The trace semantics is difficult to use to build intuitions...

In a similar way, one can define a **geometric semantics** where programs are interpreted by *directed spaces*.

A **path** in a topological space X is a continuous map $I = [0,1] \rightarrow X$.

Definition

A **d-space** (X, dX) consists of

- a topological space X
- a set dX of paths in X, called directed paths, such that
 - constant paths: every constant path is directed,
 - reparametrization: dX is closed under precomposition with increasing maps I → I, which are called reparametrizations,
 - concatenation: dX is closed under concatenation.

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Example

 (X,\leqslant) space with a partial order, $dX=\{\text{increasing maps }I\to X\}$

 \vec{l} : d-space induced by [0,1]

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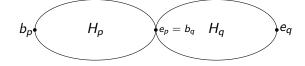
$$S^1 = \{e^{i\theta}\} 0 \leqslant \theta < 2\pi$$

 dS^1 : $p(t) = e^{if(t)}$ for some increasing function $f: I \to \mathbb{R}$



To each program p we associate a d-space (H_p, b_p, e_p) :

- *H*₁: •
- $H_{P_a} = \vec{I}$ $H_{V_a} = \vec{I}$
- H_{p,q}:



• $H_{p|q}$: $H_p \times H_q$, $b_{p|q} = (b_p, b_q)$, $e_{p|q} = (e_p, e_q)$

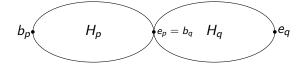
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- H_1 : •
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 H_p $e_p = b_q$ H_q e_q
- $H_{p|q}$: $H_p \times H_q$, $b_{p|q} = (b_p, b_q)$, $e_{p|q} = (e_p, e_q)$

Resource function: $r_a(x) \in \mathbb{Z}$ for each $a \in \mathcal{R}$ and point x

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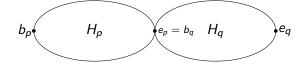
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Forbidden region:

$$F_p = \{ x \in H_p \ / \ \exists a, \quad r_a(x) < -1 \quad \text{or} \quad r_a(x) > 0 \}$$

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Geometric semantics: $G_p = H_p \setminus F_p$

$$P_a.V_a|P_a.V_a$$



$$P_a.V_a|P_a.V_a$$
 $P_a.P_b.V_b.V_a|P_b.P_a.V_a.V_b$

$$b_p$$

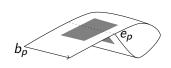
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$$P_a.V_a|P_a.V_a \qquad P_a.P_b.V_b.V_a|P_b.P_a.V_a.V_b \qquad P_a.(V_a.P_a)^*|P_a.V_a$$

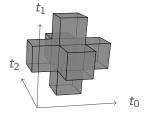
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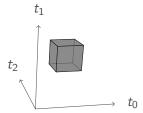




$$P_a.V_a|P_a.V_a|P_a.V_a$$
$$(\kappa_a = 1)$$



$$P_a.V_a|P_a.V_a|P_a.V_a$$
$$(\kappa_a = 2)$$



Geometric realization

The two semantics are "essentially the same": the geometric semantics is the **geometric realization** of a *cubical set*

$$G_p = \int^{n \in \square} T_p(n) \cdot \vec{I}^n$$

Proposition

Given a program p, with T_p as trace semantics and G_p as geometric semantics,

- every path $\pi:b o e$ in T_p induces a path $\overline{\pi}:b o e$ in G_p ,
- $\pi \sim \rho$ in T_p implies $\overline{\pi} \sim \overline{\rho}$ in G_p
- every path ρ of G_p is homotopic to a path $\overline{\pi}$ $(\pi$ path in $G_p)$

Computing the trace space

Goal

Given a program p, we describe an algorithm to compute a trace in each equivalence class of traces $\pi: b_p \to e_p$ up to homotopy in G_p .

Suppose given a program

$$p = p_0|p_1|\dots|p_{n-1}$$

with *n* threads.

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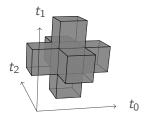
Under mild assumptions, the geometric semantics is of the form

$$G_{p} = \vec{I}^{n} \setminus \bigcup_{i=0}^{l-1} R^{i}$$

$$R^{i} = \prod_{i=0}^{n-1}]x_{j}^{i}, y_{j}^{i}[$$

where

are I open rectangles.



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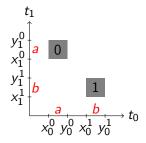
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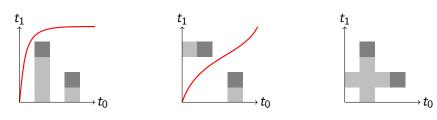
are I open rectangles.

Example

$$P_a.V_a.P_b.V_b|P_b.V_b.P_a.V_a$$

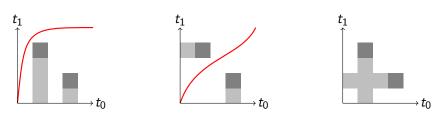


The main idea of the algorithm is to extend the forbidden cubes downwards in various directions and look whether there is a path from b to e in the resulting space.



By combining those information, we will be able to compute traces modulo homotopy.

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The directions in which to extend the holes will be coded by boolean matrices M.

 $\mathcal{M}_{l,n}$: boolean matrices with l rows and n columns.

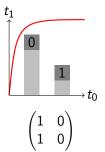
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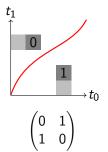
space obtained by extending for every (i,j) such that M(i,j)=1 the forbidden cube i downwards in every direction other than j

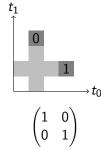
 $\mathcal{M}_{I,n}$: boolean matrices with I rows and n columns.

 X_M :

space obtained by extending for every (i,j) such that M(i,j)=1 the forbidden cube i downwards in every direction other than j



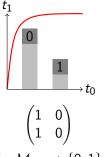


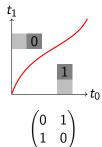


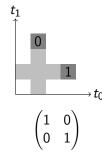
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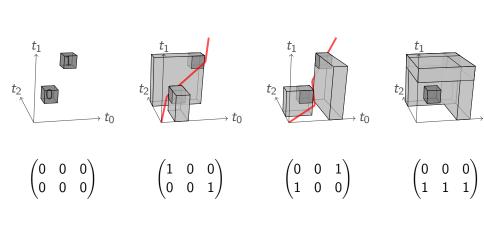


 $\Psi: \mathcal{M}_{I,n} \rightarrow \{0,1\}$:

- $\Psi(M) = 0$ if there is a path $b \to e$: M is alive
- $\Psi(M) = 1$ if there is no path $b \to e$: M is dead

alive

 $P_a.V_a.P_b.V_b \mid P_a.V_a.P_b.V_b \mid P_a.V_a.P_b.V_b$



alive

alive

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dead

- $\mathcal{M}_{I,n}$ is equipped with the pointwise ordering
- Ψ is increasing: more $1 \Rightarrow$ more obstructions
- $\mathcal{M}_{l,n}^R$: matrices with non-null rows
- $\mathcal{M}_{l,n}^{C}$: matrices with unit column vectors

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- $\mathcal{M}_{l,n}^R$: matrices with non-null rows
- $\mathcal{M}_{l,n}^{C}$: matrices with unit column vectors

Definition

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Definition

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The **dead poset**
$$D(X) = \{M \in \mathcal{M}_{l,n}^{C} / \Psi(M) = 1\}.$$

$$D(X) \longrightarrow C(X) \longrightarrow \text{homotopy classes of traces}$$

The dead poset

Proposition

A matrix $M \in \mathcal{M}_{l,n}^{\mathcal{C}}$ is in D(X) iff it satisfies

$$\forall (i,j) \in [0:I[\times[0:n[, M(i,j)=1 \Rightarrow x_j^i < \min_{i' \in R(M)} y_j^{i'}]$$

where R(M): indexes of non-null rows of M.

The dead poset

Proposition

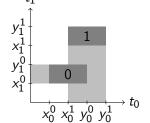
A matrix $M \in \mathcal{M}_{l,n}^{C}$ is in D(X) iff it satisfies

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where R(M): indexes of non-null rows of M.

Example

M is dead:



$$M = \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}$$

$$M = \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix} \qquad \begin{array}{c} x_1^0 = 1 < 2 = \min(y_1^0, y_1^1) \\ x_0^1 = 2 < 3 = \min(y_0^0, y_0^1) \end{array}$$

Proposition

A matrix M is in C(X) iff for every $N \in D(X)$, $N \not\leq M$.

Proposition

A matrix M is in C(X) iff for every $N \in D(X)$, $N \nleq M$.

Remark

 $N \nleq M$: there exists (i,j) s.t. N(i,j) = 1 and M(i,j) = 0.

Remark

Since C(X) is downward closed it will be enough to compute the set $C_{\text{max}}(X)$ of maximal alive matrices.

Connected components

 $M \wedge N$: pointwise min of M and N

Definition

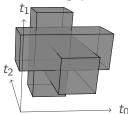
Two matrices M and N are **connected** when $M \wedge N$ does not contain any null row.

Proposition

The connected components of C(X) are in bijection with homotopy classes of traces $b \rightarrow e$ in X.

n processes p_k in parallel:

Dining philosophers



$$p_k = P_{a_k}.P_{a_{k+1}}.V_{a_k}.V_{a_{k+1}}$$

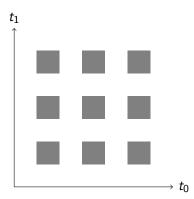
n	sched.	ALCOOL (s)	ALCOOL (MB)	SPIN (s)	SPIN (MB)
8	254	0.1	0.8	0.3	12
9	510	0.8	1.4	1.5	41
10	1022	5	4	8	179
11	2046	32	9	42	816
12	4094	227	26	313	3508
13	8190	1681	58	∞	∞
14	16382	13105	143	∞	∞

Consider the following program:

$$p = (P_a.V_a)^* | (P_a.V_a)^*$$

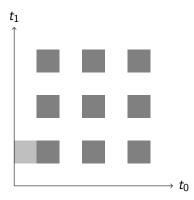
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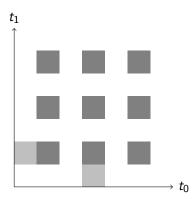
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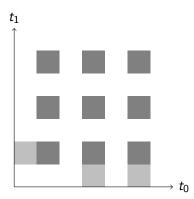
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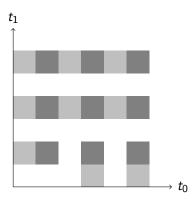
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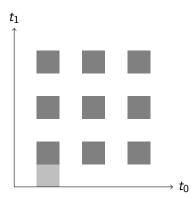
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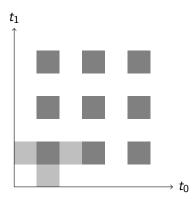
Consider the following program:

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Consider the following program:

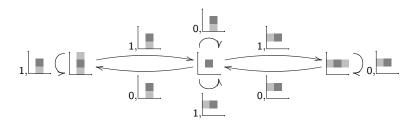
$$p = (P_a.V_a)^* | (P_a.V_a)^*$$



Consider the following program:

$$p = (P_a.V_a)^* | (P_a.V_a)^*$$

Its trace space can be described by the following automaton:



(which can be reduced a bit more)

Summary

- The computation of trace space through boolean matrices is quite efficient
- We compute a "most reduced CFG" which can be then be analyzed through usual techniques (abstract interpretation, model checking, etc.)
- Geometric semantics can be useful in order to reason about concurrency

Future works

- Interface with static analyzers
- Speed and implementation improvements (algorithms, GPU)
- Precise relation with partial-order reduction (joint work with T. Heindel)
- Lots of work remain to be done on the theoretical side in order to really understand the geometry of concurrency

Thanks!

Questions?