#### DIRECTED ALGEBRAIC TOPOLOGY

AND

## CONCURRENCY

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MPRI : Concurrency (2.3.1) - Lecture 1 -

2024 - 2025

#### A QUICK OVERVIEW

 $\mathsf{OF}$ 

#### CONCURRENCY THEORY



Texts in Theoretical Computer Science An EATCS Series

Roberto Gorrieri Cristian Versari

## Introduction to Concurrency Theory

ransition Systems and CCS

🖄 Springer

Handbook Of Process Algebra

> Edited by J.A. Bergstra A. Ponse S.A. Smolka

NORTH-HOLLAND

#### Handbook of Logic in Computer Science

**OLUME** 4

S. ABRAMSKY, DOV M. GABBAY, and T. S. E. MAIBAUM

DXFORD SCIENCE PUBLICATIONS



# GREGORY R. ANDREWS **CONCURRENT** PROGRAMMING PRINCIPLES AND PRACTICE が水和市 readers=0 v writers=0) A writers < 1



#### PARALLEL AUTOMATA META LANGUAGE

#### Syntax

Cooperating sequential processes, E. W. Dijkstra, 1965. System deadlocks, E. G. Coffman, M. J. Elphick, and A. Shoshani, 1971. The geometry of semaphore programs, S. D. Carson and P. F. Reynolds, 1987.

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- The Carson and Reynolds language is a restriction of Dijkstra's language:
  - $\cdot$  Operator || in outermost position: only sequential processes are executed in parallel
  - $\cdot$  Neither branchings nor loops

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- tokens are *owned* by processes
- conservative processes

A basic block is defined as a (finite) sequence of instructions. A program is a list of declarations, the available declarations are:

- sem <int> <set of identifiers>
 e.g. sem 3 a b c d

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  e.g. var x = 0

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- proc <identifier> = <basic block>
- init <multiset of identifiers> e.g. init a 2b 3c

## Expressions and values

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V	content of $v \in \mathcal{V}$	$x \in \mathbb{R}$	constant
$\wedge$	minimum	V	maximum
+	addition	—	substraction
*	multiplication	/	division
$\leq$	less or equal	≥	greater of equal
<	strictly less	>	strictly greater
=	equal	$\neq$	not equal
_	complement	%	modulo
		bottom	- · · ·

nullary	unary		
$\bot$ , $x \in \mathbb{R}$ , $v \in \mathcal{V}$	7		
binary			
$\land, \lor, +, -, *, /, <, >, \leqslant, \geqslant, =, \neq, \%$			

- *identifier* := *expression* the expression is evaluated then the result is stored in the identifier

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- J(*identifier*) the execution of the process is stopped and the one of a copy of *identifier* starts. There is no return mechanism.
- (L) enclose a list of instructions between parenthesis to make it a single instruction

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 $(L_1)+[e_1]+(L_2)+[e_2]+\cdots+(L_n)+[e_n]+(L_{n+1})$ 

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- If all the expressions evaluate to zero, then  $L_{n+1}$  is triggered.

The body of a process is just a (possibly empty) sequence of intructions, i.e. a basic block, separated by semicolons e.g. the Hasse/Syracuse algorithm with input value 7

proc p = x:=7; J(q)

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proc q = J(r)+[x<>1]+()
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proc p = x:=7;J(q)
proc q = J(r)+[x<>1]+()
proc r = (x:=x/2)+[x%2=0]+(x:=3*x+1) ; J(q)
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Due to the branchings, basic blocks are actually trees.

Control Flow Graphs

Control flow analysis, *F. E. Allen*, 1970 Assigning meanings to programs, *R. W. Floyd*, 1967

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- No theoretical definition yet control flow graphs must be finite for practical reasons.
- At the core of many softwares dealing with source code e.g. GCC (cf. "basic blocks"), LLVM, Frama-C.
- No such structure exist for parallel programs.

#### Generators



The Hasse-Syracuse algorithm in PAML

var x = 7

proc p = ()+[x=1]+J(q)

proc q = (x:=x/2) + [x%2=0] + (x:=3\*x+1); J(p)

init p












## Reducing the Control Flow Graph



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of the Hasse-Syracuse algorithm



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Execution traces as paths over a control flow graph
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- The (infinite) collection of paths is entirely determined by the (finite) control flow graph

The overall idea of static analysis

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Any model of a program should contain a finite representation of an overapproximation of the collection of all its execution traces.

One of the goal of the course it to provide such a structure for a large class of PAML programs.

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Abstract Machine

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- The set of expressions occuring in the program is denoted by  $\mathcal{E}.$

only depends on the current memory state

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- $\llbracket e \rrbracket = \bot$  for all expression *e* in which  $\bot$  occurs
- the other operators are interpreted as expected

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- Given a graph

$$G: A \xrightarrow[]{\partial^+} V$$

a conditional branching at vertex  $v \in V$  is a mapping

$$\beta: \{ \text{valuations} \} \rightarrow \{ a \in A \mid \partial^{\text{-}}a = v \}$$

together with a subset  $\mathcal{F}(\beta) \subseteq \mathcal{X}$  such that if the valuations  $\nu$  and  $\nu'$  match on  $\mathcal{F}(\beta)$  then  $\beta(\nu) = \beta(\nu')$ .

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- The synchronisation primitives P(s), V(s), and W(b) for  $s \in S$  and  $b \in B$ 

$$G: A \xrightarrow[\partial^+]{\partial^+} V$$
 and  $\lambda: V \to \{\text{instructions}\}$ 

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- An entry point  $v_0 \in V$  such that  $\lambda(v_0) = Skip$ .
- If  $\lambda(v) \neq Skip$ , then v has at least one outgoing arrow.
#### Abstract processes as control flow graphs

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 and  $\lambda: V \to \{\text{instructions}\}$ 

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- If  $\lambda(v) \neq Skip$ , then v has at least one outgoing arrow.
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#### Abstract processes as control flow graphs

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The arrows are interpreted as intermediate positions of the instruction pointer so a point on a control flow graph is either a vertex or an arrow.

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- The arity map  $\alpha : S \sqcup B \to \mathbb{N} \cup \{\infty\}.$
- The tuple  $(G_1, \ldots, G_n)$  of processes which are launched simultaneously at the beginning of each execution of the program.

#### Points and multi-instructions

Higher Dimensional Transition Systems, G. L. Cattani and V. Sassone, 1996

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- A multi-instruction is a partial map  $\mu : \{1, \ldots, n\} \rightarrow \{\text{instructions}\}.$

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$$\begin{array}{lll} \phi(s) & = & |\sigma(s)| \\ & + \operatorname{card}\{i \in \operatorname{dom}(\mu) \mid \mu(i) = \mathsf{P}(s)\} \\ & - \operatorname{card}\{i \in \operatorname{dom}(\mu) \mid \mu(i) = \mathsf{V}(s)\} \end{array}$$

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- for all  $b \in \mathcal{B}$ , card $\{i \in dom(\mu) \mid \mu(i) = W(b)\} \notin \{1, \dots, \alpha(b)\}$ 

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- For all  $s \in S$  the multiset  $(\sigma \cdot \mu)(s)$ , seen as a mapping from  $\{1, \ldots, n\}$  to  $\mathbb{N}$ , is given by

$$i \quad \mapsto \quad \left\{ \begin{array}{ll} \sigma(s)(i) + 1 & \text{if } i \in \text{dom}(\mu) \text{ and } \mu(i) = P(s) \\ \sigma(s)(i) - 1 & \text{if } i \in \text{dom}(\mu) \text{ and } \mu(i) = V(s) \\ \sigma(s)(i) & \text{in all other cases} \end{array} \right.$$

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A sequence  $\mu_0, \ldots, \mu_{q-1}$  of multi-intructions is said to be admissible at state  $\sigma$  when for all  $k \in \{0, \ldots, q-1\}$  the multi-instruction  $\mu_k$  is admissible at state  $\sigma \cdot \mu_0 \cdots \mu_{k-1}$ .

A directed path  $\gamma$  on  $(G_1, \ldots, G_n)$  is a sequence  $(\gamma(k))_{k \in \{0, \ldots, q\}}$  of points such that for all  $k \in \{0, \ldots, q-1\}$  we have

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-  $\mu_k(i) = \lambda_i(\gamma_i(k+1))$  for all  $k \in \{0, \dots, q-1\}$  and all  $i \in \mathsf{dom}(\mu_k)$ 

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### Admissible paths and execution traces

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Given  $\sigma$  a state of the program, a directed path is said to be admissible at  $\sigma$  when so is its associated sequence of multi-instructions at state  $\sigma$ . In this case we define the action of  $\gamma$  on the right of  $\sigma$  as follows.

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An admissible path is an execution trace when all the conditional branchings met along the way are respected: for all  $k \in \{0, ..., q-2\}$  and all  $i \in \{1, ..., n\}$  such that  $\mu_k(i)$ , which is by definition  $\lambda_i(\gamma_i(k+1))$ , is a branching, we have

 $(\mu_k(i))(\sigma \cdot \mu_0 \cdots \mu_{k-1}) = \gamma_i(k+2)$ 

#### Concurrent access

var x = 0
proc p = x := 1
proc q = x := 2
init p q



the value of x is 0



the value of x is 0





the value of  $\boldsymbol{x}$  is  $\ 1$ 



the value of x is 2









the value of x is 0



the value of x is 0



the value of x is 0



the value of  $\boldsymbol{x}$  is  $\ ?$ 

## Lack of resources

sem 1 a

proc p = P(a); V(a)

init 2p
































## Synchronisation

sync	1	b

proc p = W(b)			
init 2p			

























# Next goal

Encode admissibility into a model.

#### CONSERVATIVE PROGRAMS

**Potential Functions** 

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For all initial states  $\sigma$ , for all directed paths  $\gamma, \gamma'$  starting at the origin,

$$\partial^{\scriptscriptstyle +}\gamma = \partial^{\scriptscriptstyle +}\gamma' \quad \Rightarrow \quad \sigma\cdot\gamma|_{\mathcal{S}} = \sigma\cdot\gamma'|_{\mathcal{S}}$$

In particular, the program  $\Pi$  comes with a potential function

 $F_{\Pi}$  : {semaphores}  $\times$  {points}  $\rightarrow \mathbb{N} \cong$  {points}  $\rightarrow$  {multisets over S}

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we define a strict extension of  $\pi_n$ , by setting:

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- If the following holds for all ordered pairs of points (p, p') such that  $\partial p' = p$  or  $p' = \partial^{+}p$ , then G is conservative, otherwise it is not.

$$\pi(p') = \begin{cases} \pi(p) & \text{if } \partial^{-}p' = p \\ \pi(p) \cdot \lambda(p') & \text{if } p' = \partial^{+}p \end{cases}$$

## The discrete model of a conservative program

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- desynchronizing when there is some synchronization barrier  $b \in \mathcal{B}$  such that

$$0 \quad < \quad \mathsf{card}\big\{i \in \{1, \dots, n\} \mid \lambda_i(p_i) = \mathsf{W}(\mathsf{b})\big\} \quad \leqslant \quad \mathsf{arity}(\mathsf{b}) \;,$$

A point  $p = (p_1, \ldots, p_n)$  of the conservative program is said to be:

- conflicting when  $\lambda_i(p_i)$  and  $\lambda_j(p_j)$  conflict for some  $i \neq j$ ,
- exhausting when there is some semaphore  $s \in \mathcal{S}$  such that

$$F(p_1,\ldots,p_n,s) > \operatorname{arity}(s)$$
,

- desynchronizing when there is some synchronization barrier  $b \in \mathcal{B}$  such that

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The forbidden set gathers all the conflicting, exhausting, and desynchronizing points.

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The discrete model is the complement of its forbidden set.

{points of the program}  $\{ forbidden points \}$ 





























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## Main theorem of discrete models

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- Soundness: any directed path on a discrete model (i.e. which does not meet any forbidden point) is admissible.
- Completeness: for each admissible path which meets a forbidden point there exists a directed path which avoids them and such that both directed paths induce the same sequence of multi-instructions.

### Admissible execution trace



the value of x is 0

### Admissible execution trace



the value of x is 0

### Admissible execution trace



the value of x is 0






the value of x is 2





## Admissible execution trace avoiding forbidden points



## Admissible execution trace avoiding forbidden points



## Admissible execution trace avoiding forbidden points



## Admissible execution trace avoiding forbidden points



the value of  $\boldsymbol{x}$  is  $\ 1$ 

## Admissible execution trace avoiding forbidden points



the value of  $\boldsymbol{x}$  is  $\ 1$ 

## Admissible execution trace avoiding forbidden points



## Admissible execution trace avoiding forbidden points



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## Admissible execution trace avoiding forbidden points



# Replacement

