

# GPU computing: semantics and analysis

E. Haucourt    É. Goubault    S. Mimram    D. Rossin

September 3, 2014

## 1 Context

The postdoc position will be associated with the Computer Science Laboratory at École Polytechnique (LIX<sup>1</sup>) and will be supervised by Dominique Rossin, Éric Goubault, Samuel Mimram and Emmanuel Haucourt. The lab is composed of about a hundred researchers, and is associated with CNRS and INRIA through joint research projects. École Polytechnique is one of the most famous engineering schools in France, with a great tradition and excellence in science and technology. Students are highly selected, begin their studies at the end of undergraduate level, and can carry over until a Ph.D.

The postdoc position will also be linked to a collaborative research project involving some industrial partners in the security and IT domain, as well as the applied mathematics department (CMAP) of École Polytechnique. Mathematically, CMAP is involved in the definition of numerical filtering methods. LIX is involved in the parallelization of the code on GPUs, in particular on an NVIDIA K20 graphical card running under CUDA.

## 2 Work

The postdoc will be involved equally in theoretical studies and in practical work.

On the theoretical side, the postdoc candidate will study and formalize the semantics of GPU computing such as the ones implemented by CUDA and OpenCL, focusing on the most recent features of these parallel platforms (including dynamic parallelism, complex synchronization primitives such as compare&swap, streams, asynchronous memory copies, etc.), with both host and device primitives. The semantics should account for the underlying weak memory model, and should be tested against actual hardware implementations as well as against the current specification.

The aim of the definition of this semantics will be the verification (see for instance [8]) and the automatic parallelization of code, and will be coupled with other classical formal methods such abstract interpretation based methods [2]. A question that we wish to answer is whether we can use the recent tools from true concurrency [5] and in particular from geometric semantics [3, 4, 1], to study a realistic fragment of CUDA/OpenCL.

---

<sup>1</sup><http://www.lix.polytechnique.fr/>

This semantics should be compared with existing literature, such as [7] and [6] (even though we are more interested in high-level semantics of CUDA/OpenCL than the lower level of PTX assembly).

### 3 Miscellaneous

The salary may depend on the experience of the candidate, but should be around 2400 euros per month (net salary).

Contacts:

- `Eric.Goubault@polytechnique.edu`
- `Dominique.Rossin@polytechnique.edu`

### References

- [1] Richard Bonichon, Géraud Canet, Loïc Correnson, Eric Goubault, Emmanuel Haucourt, Michel Hirschowitz, Sébastien Labbé, and Samuel Mimram. Rigorous evidence of freedom from concurrency faults in industrial control software. In *SAFECOMP*, pages 85–98, 2011.
- [2] P. Cousot and R. Cousot. Abstract interpretation frameworks. *Journal of Logic and Computation*, 2(4):511–547, August 1992.
- [3] Lisbeth Fajstrup, Eric Goubault, Emmanuel Haucourt, Samuel Mimram, and Martin Raußen. Trace spaces: An efficient new technique for state-space reduction. In *ESOP*, pages 274–294, 2012.
- [4] Eric Goubault and Emmanuel Haucourt. A practical application of geometric semantics to static analysis of concurrent programs. In *CONCUR*, pages 503–517, 2005.
- [5] Eric Goubault and Samuel Mimram. Formal relationships between geometrical and classical models for concurrency. *Electronic Notes in Theoretical Computer Science*, 283(0):77 – 109, 2012. Proceedings of the workshop on Geometric and Topological Methods in Computer Science (GETCO).
- [6] Axel Habermaier. The model of computation of cuda and its formal semantics. Technical Report 2011-14, Informatik, 2011.
- [7] Chris Hathhorn, Michela Becchi, William L. Harrison, and Adam M. Procter. Formal semantics of heterogeneous cuda-c: A modular approach with applications. In *SSV*, pages 115–124, 2012.
- [8] Guodong Li, Peng Li, Geof Sawaya, Ganesh Gopalakrishnan, Indradeep Ghosh, and Sreeranga P. Rajan. Gklee: Concolic verification and test generation for gpus. *SIGPLAN Not.*, 47(8):215–224, February 2012.